

**DM74AS574WM Information**


For Reference Only

**Part Number** [DM74AS574WM](#)  
**Manufacturer** ON Semiconductor  
**Category** Integrated Circuits (ICs)  
[Logic - Flip Flops](#)  
**Description** IC FF D-TYPE SNGL 8BIT 20SOIC  
**Package** 20-SOIC (0.295", 7.50mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**DM74AS574WM Specifications**

Manufacturer Part Number	<a href="#">DM74AS574WM</a>
Manufacturer	ON Semiconductor
Category	Integrated Circuits (ICs) <a href="#">Logic - Flip Flops</a>
Package	20-SOIC (0.295", 7.50mm Width)
Series	74AS
Function	Standard
Type	D-Type
Output Type	Tri-State, Non-Inverted
Number of Elements	1
Number of Bits per Element	8
Clock Frequency	80MHz
Max Propagation Delay @ V, Max CL	9ns @ 5V, 50pF
Trigger Type	Positive Edge
Current - Output High, Low	15mA, 48mA
Voltage - Supply	4.5 V ~ 5.5 V
Current - Quiescent (Iq)	116mA
Input Capacitance	-
Operating Temperature	0°C ~ 70°C (TA)
Mounting Type	Surface Mount
Package / Case	20-SOIC (0.295", 7.50mm Width)

[Report errors?](#)

## DM74AS574WM Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## DM74AS574WM Payment Methods



## DM74AS574WM Shipping Methods



If you have any question about DM74AS574WM, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)