

# DS12CR887-5+

#### **DS12CR887-5+ Information**

www.nelsener.com	Part Number Manufacturer Category Description Package	Maxim Integrated Integrated Circuits (ICs) Clock/Timing - Real Time Clocks IC RTC CLK/CALENDAR PAR 24-EDIP 24-DIP Module (0.600", 15.24mm)	
	Package	For the pricing/inventory/lead time, please contact us	
For Reference Only		Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



## **DS12CR887-5+ Specifications**

		Report errors?	
Supplier Device Package	24-EDIP		
Package / Case	24-DIP Module (0.600", 15.24mm)		
Mounting Type	Through Hole		
Operating Temperature	$-40^{\circ}\mathrm{C} \sim 85^{\circ}\mathrm{C}$		
Current - Timekeeping (Max)	2mA @ 4.5V ~ 5.5V		
Voltage - Supply, Battery	2 V ~ 3.05 V		
Voltage - Supply	4.5 V ~ 5.5 V		
Interface	Parallel		
Date Format	YY-MM-DD-dd		
Time Format	HH:MM:SS (12/24 hr)		
Memory Size	114B		
Features	Alarm, Leap Year, NVSRAM, Square Wave Output, Trickle-Charger		
Туре	Clock/Calendar		
Series	-		
Package	24-DIP Module (0.600", 15.24mm)		
	Clock/Timing - Real Time Clocks		
Category	Integrated Circuits (ICs)		
Manufacturer	Maxim Integrated		
Manufacturer Part Number	DS12CR887-5+		

#### DS12CR887-5+ Guarantees



Quality Guarantees

We provide 90 days warranty. \* If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

# SERVICE EUARANTEE

#### **Service Guarantees**

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

# DS12CR887-5+ Payment Methods



### DS12CR887-5+ Shipping Methods



If you have any question about DS12CR887-5+, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com