



### **ICS281PGIT Information**

Part Number ICS281PGIT

Manufacturer IDT, Integrated Device Technology Inc

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description** IC CLK SYNTH TRPL PLL 16-TSSOP

**Package** 16-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

# **Certified Quality**

For Reference Only

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









# **ICS281PGIT Specifications**

Manufacturer Part Number	ICS281PGIT
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs)
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers
Package	16-TSSOP (0.173", 4.40mm Width)
Series	VersaClock?
Type	Clock/Frequency Synthesizer
PLL	Yes with Bypass
Input	Clock, Crystal
Output	CMOS
Number of Circuits	1
Ratio - Input:Output	1:3
Differential - Input:Output	No/No
Frequency - Max	200MHz
Divider/Multiplier	Yes/Yes
Voltage - Supply	3.135 V ~ 3.465 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	16-TSSOP (0.173", 4.40mm Width)
Supplier Device Package	16-TSSOP
	Report errors?

### **ICS281PGIT Guarantees**



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

# **ICS281PGIT Payment Methods**



















### **ICS281PGIT Shipping Methods**













If you have any question about ICS281PGIT, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com