



ICS570GI-01 Information

www.hellsmer.com

For Reference Only

Part Number ICS570GI-01

Manufacturer IDT, Integrated Device Technology Inc

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

Description IC MULTIPLIER/ZDB 8-MSOP

Package 8-TSSOP, 8-MSOP (0.118", 3.00mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









ICS570GI-01 Specifications

Manufacturer Part Number	ICS570GI-01	
Manufacturer	IDT, Integrated Device Technology Inc	
Category	Integrated Circuits (ICs)	
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)	
Series	-	
Type	Zero Delay Buffer, Spread Spectrum Clock Generator, Zero Delay Buffer	
PLL	Yes	
Input	Clock	
Output	CMOS	
Number of Circuits	1	
Ratio - Input:Output	1:1	
Differential - Input:Output	No/No	
Frequency - Max	55MHz	
Divider/Multiplier	No/Yes	
Voltage - Supply	3.15 V ~ 3.45 V	
Operating Temperature	-40°C ~ 85°C	
Mounting Type	Surface Mount	
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)	
Supplier Device Package	8-TSSOP	
		Report errors?

ICS570GI-01 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

ICS570GI-01 Payment Methods



















ICS570GI-01 Shipping Methods













If you have any question about ICS570GI-01, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com