

ICS581G-02T Information

Part Number ICS581G-02T

Manufacturer IDT, Integrated Device Technology Inc

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

Description IC CLK MUX GLITCH FREE 16-TSSOP

Package 16-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

For Reference Only

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









ICS581G-02T Specifications

ICS581G-02T	
IDT, Integrated Device Technology Inc	
Integrated Circuits (ICs)	
Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
16-TSSOP (0.173", 4.40mm Width)	
ClockBlocks?	
Fanout Buffer (Distribution), Zero Delay Buffer	
Yes	
Clock	
Clock	
1	
2:4	
No/No	
200MHz	
Yes/No	
3 V ~ 5.5 V	
0°C ~ 70°C	
Surface Mount	
16-TSSOP (0.173", 4.40mm Width)	
16-TSSOP	
	Report errors?
	IDT, Integrated Device Technology Inc Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers 16-TSSOP (0.173", 4.40mm Width) ClockBlocks? Fanout Buffer (Distribution), Zero Delay Buffer Yes Clock Clock 1 2:4 No/No 200MHz Yes/No 3 V ~ 5.5 V 0°C ~ 70°C Surface Mount 16-TSSOP (0.173", 4.40mm Width)

ICS581G-02T Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

ICS581G-02T Payment Methods



















ICS581G-02T Shipping Methods













If you have any question about ICS581G-02T, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com