



#### **IDT23S05E-1DCI Information**

Part Number IDT23S05E-1DCI

Manufacturer IDT, Integrated Device Technology Inc

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description** IC CLK BUFFER PLL STD DRV 8-SOIC

**Package** 8-SOIC (0.154", 3.90mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

## **Certified Quality**

For Reference Only

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## **IDT23S05E-1DCI Specifications**

Manufacturer Part Number	IDT23S05E-1DCI
Manufacturer	
	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs)
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers
Package	8-SOIC (0.154", 3.90mm Width)
Series	-
Type	Zero Delay Buffer
PLL	Yes with Bypass
Input	LVTTL
Output	CMOS, LVTTL
Number of Circuits	1
Ratio - Input:Output	1:5
Differential - Input:Output	No/No
Frequency - Max	200MHz
Divider/Multiplier	No/No
Voltage - Supply	3 V ~ 3.6 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	8-SOIC (0.154", 3.90mm Width)
Supplier Device Package	8-SOIC
	Report errors?

#### **IDT23S05E-1DCI Guarantees**



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

# **IDT23S05E-1DCI Payment Methods**



















### **IDT23S05E-1DCI Shipping Methods**













If you have any question about IDT23S05E-1DCI, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com