



### **IDT23S09E-1HPG8 Information**

Part Number IDT23S09E-1HPG8

Manufacturer IDT, Integrated Device Technology Inc

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description** IC CLK BUFFER ZD HI DRV 16-TSSOP

**Package** 16-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

## **Certified Quality**

For Reference Only

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## **IDT23S09E-1HPG8 Specifications**

Manufacturer Part Number	IDT23S09E-1HPG8
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs)
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers
Package	16-TSSOP (0.173", 4.40mm Width)
Series	-
Type	Zero Delay Buffer
PLL	Yes with Bypass
Input	LVTTL
Output	CMOS, LVTTL
Number of Circuits	1
Ratio - Input:Output	1:9
Differential - Input:Output	No/No
Frequency - Max	200MHz
Divider/Multiplier	No/No
Voltage - Supply	3 V ~ 3.6 V
Operating Temperature	0°C ~ 70°C
Mounting Type	Surface Mount
Package / Case	16-TSSOP (0.173", 4.40mm Width)
Supplier Device Package	16-TSSOP
	Report errors?

### **IDT23S09E-1HPG8** Guarantees



### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## IDT23S09E-1HPG8 Payment Methods



















# IDT23S09E-1HPG8 Shipping Methods













If you have any question about IDT23S09E-1HPG8, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com