



#### **IDT5T9820NLGI Information**



For Reference Only

Part Number IDT5T9820NLGI

Manufacturer IDT, Integrated Device Technology Inc

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description** IC CLK DRIVER ZD PLL 68-VFQFPN

Package 68-VFQFN Exposed Pad

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









# **IDT5T9820NLGI Specifications**

Manufacturer Part Number	IDT5T9820NLGI
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs)
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers
Package	68-VFQFN Exposed Pad
Series	-
Туре	PLL Clock Driver
PLL	Yes with Bypass
Input	eHSTL, HSTL, LVPECL, LVTTL
Output	eHSTL, HSTL, LVTTL
Number of Circuits	1
Ratio - Input:Output	2:10
Differential - Input:Output	Yes/No
Frequency - Max	250MHz
Divider/Multiplier	Yes/No
Voltage - Supply	2.3 V ~ 2.7 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	68-VFQFN Exposed Pad
Supplier Device Package	68-VFQFPN (10x10)
	Report errors?

### **IDT5T9820NLGI Guarantees**



### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

# **IDT5T9820NLGI Payment Methods**





















### **IDT5T9820NLGI Shipping Methods**













If you have any question about IDT5T9820NLGI, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com