

**IR2011SPBF Information**


For Reference Only

**Part Number** [IR2011SPBF](#)  
**Manufacturer** Infineon Technologies  
**Category** Integrated Circuits (ICs)  
[PMIC - Gate Drivers](#)  
**Description** HI/LO SIDE DRVR 8SOIC  
**Package** 8-SOIC (0.154", 3.90mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**IR2011SPBF Specifications**

Manufacturer Part Number	<a href="#">IR2011SPBF</a>
Manufacturer	Infineon Technologies
Category	Integrated Circuits (ICs) <a href="#">PMIC - Gate Drivers</a>
Package	8-SOIC (0.154", 3.90mm Width)
Series	-
Driven Configuration	Half-Bridge
Channel Type	Independent
Number of Drivers	2
Gate Type	N-Channel MOSFET
Voltage - Supply	10 V ~ 20 V
Logic Voltage - VIL, VIH	0.7V, 2.2V
Current - Peak Output (Source, Sink)	1A, 1A
Input Type	Inverting
High Side Voltage - Max (Bootstrap)	200V
Rise / Fall Time (Typ)	35ns, 20ns
Operating Temperature	-40°C ~ 150°C (TJ)
Mounting Type	Surface Mount
Package / Case	8-SOIC (0.154", 3.90mm Width)
Supplier Device Package	8-SOIC

[Report errors?](#)

## IR2011SPBF Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## IR2011SPBF Payment Methods



## IR2011SPBF Shipping Methods



If you have any question about IR2011SPBF, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)