

**IS42S32200C1-7TL-TR Information**


For Reference Only

**Part Number** [IS42S32200C1-7TL-TR](#)  
**Manufacturer** ISSI, Integrated Silicon Solution Inc  
**Category** Integrated Circuits (ICs)  
[Memory](#)  
**Description** IC SDRAM 64MBIT 143MHZ 86TSOP  
**Package** 86-TFSOP (0.400", 10.16mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**IS42S32200C1-7TL-TR Specifications**

Manufacturer Part Number	<a href="#">IS42S32200C1-7TL-TR</a>
Manufacturer	ISSI, Integrated Silicon Solution Inc
Category	Integrated Circuits (ICs) <a href="#">Memory</a>
Package	86-TFSOP (0.400", 10.16mm Width)
Series	-
Memory Type	Volatile
Memory Format	DRAM
Technology	SDRAM
Memory Size	64Mb (2M x 32)
Memory Interface	Parallel
Clock Frequency	143MHz
Write Cycle Time - Word, Page	-
Access Time	5.5ns
Voltage - Supply	3.15 V ~ 3.45 V
Operating Temperature	0°C ~ 70°C (TA)
Mounting Type	Surface Mount
Package / Case	86-TFSOP (0.400", 10.16mm Width)
Supplier Device Package	86-TSOP II

[Report errors?](#)

## IS42S32200C1-7TL-TR Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## IS42S32200C1-7TL-TR Payment Methods



## IS42S32200C1-7TL-TR Shipping Methods



If you have any question about IS42S32200C1-7TL-TR, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)