

**ISL6146EFUZ Information**


For Reference Only

**Part Number** [ISL6146EFUZ](#)  
**Manufacturer** Intersil  
**Category** Integrated Circuits (ICs)  
           [PMIC - OR Controllers, Ideal Diodes](#)  
**Description** IC OR CTRLR N+1 8MSOP  
**Package** 8-TSSOP, 8-MSOP (0.118", 3.00mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**ISL6146EFUZ Specifications**

Manufacturer Part Number	<a href="#">ISL6146EFUZ</a>
Manufacturer	Intersil
Category	Integrated Circuits (ICs) <a href="#">PMIC - OR Controllers, Ideal Diodes</a>
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Series	-
Type	N+1 ORing Controller
FET Type	N-Channel
Ratio - Input:Output	N:1
Internal Switch(s)	No
Delay Time - ON	-
Delay Time - OFF	65ns
Current - Output (Max)	-
Current - Supply	25µA
Voltage - Supply	3 V ~ 20 V
Applications	N+1 Power Supplies, Telecom/Datacom Systems
Operating Temperature	-40°C ~ 125°C
Mounting Type	Surface Mount
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Supplier Device Package	8-MSOP

[Report errors?](#)

## ISL6146EFUZ Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## ISL6146EFUZ Payment Methods



## ISL6146EFUZ Shipping Methods



If you have any question about ISL6146EFUZ, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)