



### **ISL6522CR-T Information**



For Reference Only

Part Number ISL6522CR-T

Manufacturer Intersil

Category Integrated Circuits (ICs)

PMIC - Voltage Regulators - DC DC Switching

Controllers

**Description** IC REG CTRLR BUCK 16QFN

Package 16-VQFN Exposed Pad

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









# **ISL6522CR-T Specifications**

Manufacturer Part Number	ISL6522CR-T
Manufacturer	Intersil
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - DC DC Switching Controllers
Package	16-VQFN Exposed Pad
Series	-
Output Type	Transistor Driver
Function	Step-Down
Output Configuration	Positive
Topology	Buck
Number of Outputs	1
Output Phases	1
Voltage - Supply (Vcc/Vdd)	10.8 V ~ 13.2 V
Frequency - Switching	200kHz
Duty Cycle (Max)	100%
Synchronous Rectifier	Yes
Clock Sync	No
Serial Interfaces	-
Control Features	Enable, Frequency Control, Phase Control, Soft Start
Operating Temperature	$0^{\circ}\text{C} \sim 70^{\circ}\text{C} \text{ (TA)}$
Package / Case	16-VQFN Exposed Pad
Supplier Device Package	16-QFN-EP (5x5)
	Report errors?

### **ISL6522CR-T Guarantees**



### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

# **ISL6522CR-T Payment Methods**



















### **ISL6522CR-T Shipping Methods**













If you have any question about ISL6522CR-T, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com