

## ISL78208ARZ-T

Request a Quote

### **ISL78208ARZ-T Information**

Contraction for the second sec	Part Number	ISL78208ARZ-T
	Manufacturer	Intersil
	Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Regulators
	Description	IC REG BCK ADJ 3A DL SYNC 32-QFN
	Package	32-VFQFN Exposed Pad
For Reference Only		For the pricing/inventory/lead time, please contact
		us
		Website: https://www.heisener.com
		E-mail: salesdept@heisener.com



Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



## **ISL78208ARZ-T Specifications**

Manufacturer Part Number	ISL78208ARZ-T	
Manufacturer	Intersil	
Category	Integrated Circuits (ICs)	
	PMIC - Voltage Regulators - DC DC Switching Regulators	
Package	32-VFQFN Exposed Pad	
Series	-	
Function	Step-Down	
Output Configuration	Positive	
Topology	Buck	
Output Type	Adjustable	
Number of Outputs	2	
Voltage - Input (Min)	4.5V	
Voltage - Input (Max)	28V	
Voltage - Output (Min/Fixed)	0.8V	
Voltage - Output (Max)	26V	
Current - Output	3A	
Frequency - Switching	300kHz ~ 2MHz	
Synchronous Rectifier	Yes	
Operating Temperature	-40°C ~ 105°C (TA)	
Mounting Type	Surface Mount	
Package / Case	32-VFQFN Exposed Pad	
Supplier Device Package	32-QFN (5x5)	
	Report errors?	

Report errors?

### **ISL78208ARZ-T** Guarantees



Quality Guarantees

We provide 90 days warranty. \* If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

# SERVICE BUARANTEE

#### **Service Guarantees**

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

## **ISL78208ARZ-T Payment Methods**



## ISL78208ARZ-T Shipping Methods



If you have any question about ISL78208ARZ-T, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com