



ISL78302ARNBZ Information

Part Number ISL78302ARNBZ

Manufacturer Intersil

Category Integrated Circuits (ICs)

PMIC - Voltage Regulators - Linear

Description IC REG LIN 1.5/3.3V 10DFN

Package 10-VFDFN Exposed Pad For the pricing/inventory/lead time, please contact

Website: https://www.heisener.com For Reference Only

E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









ISL78302ARNBZ Specifications

Manufacturer Part Number	ISL78302ARNBZ
Manufacturer	Intersil
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - Linear
Package	10-VFDFN Exposed Pad
Series	Automotive, AEC-Q100
Output Configuration	Positive
Output Type	Fixed
Number of Regulators	2
Voltage - Input (Max)	6.5V
Voltage - Output (Min/Fixed)	1.5V, 3.3V
Voltage - Output (Max)	-
Voltage Dropout (Max)	-, 0.3V @ 300mA (Typ)
Current - Output	300mA, 300mA
Current - Quiescent (Iq)	-
Current - Supply (Max)	$36\mu A \sim 55\mu A$
PSRR	64dB ~ 38dB (1kHz ~ 100kHz), -
Control Features	Enable, Power On Reset
Protection Features	Over Current, Over Temperature
Operating Temperature	-40°C ~ 105°C
Mounting Type	Surface Mount
Package / Case	10-VFDFN Exposed Pad
Supplier Device Package	10-DFN (3x3)
	Report errors?

ISL78302ARNBZ Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

ISL78302ARNBZ Payment Methods



















ISL78302ARNBZ Shipping Methods













If you have any question about ISL78302ARNBZ, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com