

**LMX2320TM Information**


For Reference Only

**Part Number** [LMX2320TM](#)  
**Manufacturer** Texas Instruments  
**Category** Integrated Circuits (ICs)  
[Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers](#)  
**Description** IC FREQ SYNTH 2.0GHZ 20-TSSOP  
**Package** 20-TSSOP (0.173", 4.40mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**LMX2320TM Specifications**

Manufacturer Part Number	<a href="#">LMX2320TM</a>
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers</a>
Package	20-TSSOP (0.173", 4.40mm Width)
Series	PLLatinum?
Type	PLL Frequency Synthesizer
PLL	Yes
Input	CMOS, TTL
Output	CMOS
Number of Circuits	1
Ratio - Input:Output	1:1
Differential - Input:Output	Yes/No
Frequency - Max	2GHz
Divider/Multiplier	Yes/No
Voltage - Supply	2.7 V ~ 5.5 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	20-TSSOP (0.173", 4.40mm Width)
Supplier Device Package	20-TSSOP

[Report errors?](#)

## LMX2320TM Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## LMX2320TM Payment Methods



## LMX2320TM Shipping Methods



If you have any question about LMX2320TM, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)