

## LMX2430TMX/NOPB

### LMX2430TMX/NOPB Information



For Reference Only

Part NumberLMX2430TMX/NOPBManufacturerTexas InstrumentsCategoryIntegrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description**IC PLL FREQ SYNTH 3GHZ TSSOP**Package**20-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## LMX2430TMX/NOPB Specifications

Manufacturer Part Number	LMX2430TMX/NOPB	
Manufacturer	Texas Instruments	
Category	Integrated Circuits (ICs)	
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
Package	20-TSSOP (0.173", 4.40mm Width)	
Series	PLLatinum?	
Type	PLL Frequency Synthesizer	
PLL	Yes with Bypass	
Input	CMOS	
Output	CMOS	
Number of Circuits	1	
Ratio - Input:Output	3:2	
Differential - Input:Output	Yes/No	
Frequency - Max	3GHz, 800MHz	
Divider/Multiplier	No/No	
Voltage - Supply	2.25 V ~ 2.75 V	
Operating Temperature	-40°C ~ 85°C	
Mounting Type	Surface Mount	
Package / Case	20-TSSOP (0.173", 4.40mm Width)	
Supplier Device Package	20-TSSOP	
	Report error	:s?

#### LMX2430TMX/NOPB Guarantees



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

### LMX2430TMX/NOPB Payment Methods



















## LMX2430TMX/NOPB Shipping Methods













If you have any question about LMX2430TMX/NOPB, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com