

LP3982IMMX-ADJ

a Quote

LP3982IMMX-ADJ Information

www.bettemer.com	Part Number	LP3982IMMX-ADJ	
	Manufacturer	Texas Instruments	131592
	Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - Linear	- 1972 1973
	Description	IC REG LIN POS ADJ 300MA 8VSSOP	- 2313
	Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)	- A.S
		For the pricing/inventory/lead time, please contact	E106
For Reference Only		us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



LP3982IMMX-ADJ Specifications

Manufacturer Part Number	LP3982IMMX-ADJ
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - Linear
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Series	-
Output Configuration	Positive
Output Type	Adjustable
Number of Regulators	1
Voltage - Input (Max)	6V
Voltage - Output (Min/Fixed)	1.25V
Voltage - Output (Max)	6V
Voltage Dropout (Max)	220mV
Current - Output	300mA
Current - Quiescent (Iq)	-
Current - Supply (Max)	270μΑ
PSRR	-
Control Features	Enable
Protection Features	Over Current, Over Temperature
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Supplier Device Package	8-VSSOP
	Report error

LP3982IMMX-ADJ Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

LP3982IMMX-ADJ Payment Methods



LP3982IMMX-ADJ Shipping Methods



If you have any question about LP3982IMMX-ADJ, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com