

# M1026-13-155.5200

#### M1026-13-155.5200 Information

Heisener.com	Part Number Manufacturer Category Description Package	M1026-13-155.5200 IDT, Integrated Device Technology Inc Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers IC PLL JITTER ATTENUATOR 36CLCC 36-CLCC	
For Reference Only	U	For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

## **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



## M1026-13-155.5200 Specifications

Manufacturer Part Number	M1026-13-155.5200		
Manufacturer	IDT, Integrated Device Technology Inc		
Category	Integrated Circuits (ICs)		
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers		
Package	36-CLCC		
Series	-		
Туре	-		
PLL	Yes		
Input	LVCMOS, LVDS, LVPECL, LVTTL		
Output	LVPECL		
Number of Circuits	1		
Ratio - Input:Output	2:1		
Differential - Input:Output	Yes/Yes		
Frequency - Max	155.52MHz		
Divider/Multiplier	Yes/No		
Voltage - Supply	3.135 V ~ 3.465 V		
Operating Temperature	$0^{\circ}$ C ~ $70^{\circ}$ C		
Mounting Type	Surface Mount		
Package / Case	36-CLCC		
Supplier Device Package	36-CLCC (9x9)		
	Report errors?		

#### M1026-13-155.5200 Guarantees



Quality Guarantees

We provide 90 days warranty. \* If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

# SERVICE BUARANTEE

#### **Service Guarantees**

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

#### M1026-13-155.5200 Payment Methods



#### M1026-13-155.5200 Shipping Methods



If you have any question about M1026-13-155.5200, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com