

**M74HC51RM13TR Information**


For Reference Only

**Part Number** [M74HC51RM13TR](#)  
**Manufacturer** STMicroelectronics  
**Category** Integrated Circuits (ICs)  
[Logic - Gates and Inverters - Multi-Function, Configurable](#)  
**Description** IC INVERTER GATE DUAL 14-SOIC  
**Package** 14-SOIC (0.154", 3.90mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)



[Request a Quote](#)

**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**M74HC51RM13TR Specifications**

Manufacturer Part Number	<a href="#">M74HC51RM13TR</a>
Manufacturer	STMicroelectronics
Category	Integrated Circuits (ICs) <a href="#">Logic - Gates and Inverters - Multi-Function, Configurable</a>
Package	14-SOIC (0.154", 3.90mm Width)
Series	74HC
Logic Type	AND/OR/INVERT Gate
Number of Circuits	2
Number of Inputs	10 Input (3, 3, 2, 2)
Schmitt Trigger Input	No
Output Type	Single-Ended
Current - Output High, Low	5.2mA, 5.2mA
Voltage - Supply	2 V ~ 6 V
Operating Temperature	-55°C ~ 125°C
Mounting Type	Surface Mount
Package / Case	14-SOIC (0.154", 3.90mm Width)
Supplier Device Package	14-SO

[Report errors?](#)

## M74HC51RM13TR Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## M74HC51RM13TR Payment Methods



## M74HC51RM13TR Shipping Methods



If you have any question about M74HC51RM13TR, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)