

MAX17544ATP+

Quote

MAX17544ATP+ Information

	Part Number	MAX17544ATP+	
COLUMN STATE	Manufacturer	Maxim Integrated	121 2019
	Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Regulators	
	Description	IC REG BUCK ADJ 3.5A SYNC 20TQFN	- 35 26
	Package	20-WQFN Exposed Pad	i in i i i i i
		For the pricing/inventory/lead time, please contact	
For Reference Only		us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



MAX17544ATP+ Specifications

Manufacturer Part Number	MAX17544ATP+	
Manufacturer	Maxim Integrated	
Category	Integrated Circuits (ICs)	
	PMIC - Voltage Regulators - DC DC Switching Regulators	
Package	20-WQFN Exposed Pad	
Series	Himalaya	
Function	Step-Down	
Output Configuration	Positive	
Topology	Buck	
Output Type	Adjustable	
Number of Outputs	1	
Voltage - Input (Min)	4.5V	
Voltage - Input (Max)	42V	
Voltage - Output (Min/Fixed)	0.9V	
Voltage - Output (Max)	37.8V	
Current - Output	3.5A	
Frequency - Switching	100kHz ~ 2.2MHz	
Synchronous Rectifier	Yes	
Operating Temperature	-40°C ~ 125°C (TJ)	
Mounting Type	Surface Mount	
Package / Case	20-WQFN Exposed Pad	
Supplier Device Package	20-TQFN (4x4)	
	Report errors?	

Report errors?

MAX17544ATP+ Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE EUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

MAX17544ATP+ Payment Methods



MAX17544ATP+ Shipping Methods



If you have any question about MAX17544ATP+, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com