

MAX17605AUA+T

Quote

MAX17605AUA+T Information

	Part Number	MAX17605AUA+T	
www.haidlar.com	Manufacturer	Maxim Integrated	E 1 2 3
	Category	Integrated Circuits (ICs) PMIC - Gate Drivers	
	Description	IC MOSFET DRVR 4A DUAL 8UMAX	- 742
	Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width) Exposed Pad	ness Talls
For Reference Only		For the pricing/inventory/lead time, please contact	بربواها
		us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



MAX17605AUA+T Specifications

Manufacturer Part Number	MAX17605AUA+T	
Manufacturer	Maxim Integrated	
Category	Integrated Circuits (ICs)	
	PMIC - Gate Drivers	
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width) Exposed Pad	
Series	-	
Driven Configuration	Low-Side	
Channel Type	Independent	
Number of Drivers	2	
Gate Type	IGBT, SiC MOSFET	
Voltage - Supply	4 V ~ 14 V	
Logic Voltage - VIL, VIH	2V, 4.25V	
Current - Peak Output (Source, Sink)	4A, 4A	
Input Type	Inverting, Non-Inverting	
High Side Voltage - Max (Bootstrap)	-	
Rise / Fall Time (Typ)	40ns, 25ns	
Operating Temperature	-40°C ~ 150°C (TJ)	
Mounting Type	Surface Mount	
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width) Exposed Pad	
Supplier Device Package	8-uMax-EP	
		Report errors?

MAX17605AUA+T Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE BUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

MAX17605AUA+T Payment Methods



MAX17605AUA+T Shipping Methods



If you have any question about MAX17605AUA+T, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com