



### **MAX5053BEUA Information**



For Reference Only

Part Number MAX5053BEUA

Manufacturer Maxim Integrated

Category Integrated Circuits (ICs)

PMIC - Voltage Regulators - DC DC Switching

Controllers

**Description** IC REG CTRLR BOOST 8UMAX

Package 8-TSSOP, 8-MSOP (0.118", 3.00mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

## **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## **MAX5053BEUA Specifications**

Manufacturer Part Number	MAX5053BEUA
Manufacturer	Maxim Integrated
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - DC DC Switching Controllers
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Series	-
Output Type	Transistor Driver
Function	Step-Up
Output Configuration	Positive, Isolation Capable
Topology	Boost
Number of Outputs	1
Output Phases	1
Voltage - Supply (Vcc/Vdd)	10.8 V ~ 24 V
Frequency - Switching	262kHz
Duty Cycle (Max)	75%
Synchronous Rectifier	No
Clock Sync	No
Serial Interfaces	-
Control Features	Enable
Operating Temperature	-40°C ~ 85°C (TA)
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Supplier Device Package	8-uMAX
	Report errors?

### **MAX5053BEUA Guarantees**



### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

# **MAX5053BEUA Payment Methods**



















## **MAX5053BEUA Shipping Methods**













If you have any question about MAX5053BEUA, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com