

MAX5069DAUE+T

MAX5069DAUE+T Information

ettititi www.philatonee	 MAX5069DAUE+T Maxim Integrated Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Controllers IC REG CTRLR FWRD CONV 16TSSOP 16-TSSOP (0.173", 4.40mm Width) Exposed Pad	
For Reference Only	For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



MAX5069DAUE+T Specifications

Manufacturer Part Number	MAX5069DAUE+T
Manufacturer	Maxim Integrated
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - DC DC Switching Controllers
Package	16-TSSOP (0.173", 4.40mm Width) Exposed Pad
Series	-
Output Type	Transistor Driver
Function	Step-Up/Step-Down
Output Configuration	Positive, Isolation Capable
Topology	Forward Converter
Number of Outputs	1
Output Phases	1
Voltage - Supply (Vcc/Vdd)	10.8 V ~ 24 V
Frequency - Switching	25kHz ~ 1.25MHz
Duty Cycle (Max)	100%
Synchronous Rectifier	No
Clock Sync	Yes
Serial Interfaces	-
Control Features	Dead Time Control, Enable, Frequency Control
Operating Temperature	-40°C ~ 125°C (TJ)
Package / Case	16-TSSOP (0.173", 4.40mm Width) Exposed Pad
Supplier Device Package	16-TSSOP-EP
	Report errors

MAX5069DAUE+T Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE BUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

MAX5069DAUE+T Payment Methods



MAX5069DAUE+T Shipping Methods



If you have any question about MAX5069DAUE+T, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com