

MAX5098AATJ+T

MAX5098AATJ+T Information

Where and the sure of the sure		MAX5098AATJ+T Maxim Integrated Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Regulators	
	Description	IC REG BUCK BOOST ADJ DL 32TQFN	- 26 (2 52)
	Package	32-WFQFN Exposed Pad	「同業で出た
For Reference Only		For the pricing/inventory/lead time, please contact us	
		Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



MAX5098AATJ+T Specifications

Manufacturer Part Number	MAX5098AATJ+T
Manufacturer	Maxim Integrated
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - DC DC Switching Regulators
Package	32-WFQFN Exposed Pad
Series	-
Function	Step-Up, Step-Down
Output Configuration	Positive
Topology	Buck, Boost
Output Type	Adjustable
Number of Outputs	2
Voltage - Input (Min)	4.5V
Voltage - Input (Max)	19V
Voltage - Output (Min/Fixed)	0.8V, 4.5V
Voltage - Output (Max)	16.15V, 28V
Current - Output	1.75A (Switch), 2.8A (Switch)
Frequency - Switching	200kHz ~ 2.2MHz
Synchronous Rectifier	No
Operating Temperature	-40°C ~ 125°C (TJ)
Mounting Type	Surface Mount
Package / Case	32-WFQFN Exposed Pad
Supplier Device Package	32-TQFN-EP (5x5)
	Report errors?

Report errors?

MAX5098AATJ+T Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE BUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

MAX5098AATJ+T Payment Methods



MAX5098AATJ+T Shipping Methods



If you have any question about MAX5098AATJ+T, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com