

MAX5101BEUE+T

Quote

MAX5101BEUE+T Information

	Part Number	MAX5101BEUE+T	
Strew helsener.com	Manufacturer	Maxim Integrated	
	Category	Integrated Circuits (ICs) Data Acquisition - Digital to Analog Converters (DAC)	
	Description	IC DAC 8BIT TRPL R-R 16-TSSOP	5.506
	Package	16-TSSOP (0.173", 4.40mm Width)	_ ≣ 1284
For Reference Only		For the pricing/inventory/lead time, please contact us	
		Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Q

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



MAX5101BEUE+T Specifications

Manufacturer Part Number	MAX5101BEUE+T		
Manufacturer	Maxim Integrated		
Category	Integrated Circuits (ICs)		
	Data Acquisition - Digital to Analog Converters (DAC)		
Package	16-TSSOP (0.173", 4.40mm Width)		
Series	-		
Number of Bits	8		
Number of D/A Converters	3		
Settling Time	бµѕ (Тур)		
Output Type	Voltage - Buffered		
Differential Output	No		
Data Interface	Parallel		
Reference Type	Internal		
Voltage - Supply, Analog	2.7 V ~ 5.5 V		
Voltage - Supply, Digital	2.7 V ~ 5.5 V		
INL/DNL (LSB)	±2 (Max), ±1 (Max)		
Architecture	String DAC		
Operating Temperature	-40°C ~ 85°C		
Package / Case	16-TSSOP (0.173", 4.40mm Width)		
Supplier Device Package	16-TSSOP		
Mounting Type	-		
	Report errors ⁴		

MAX5101BEUE+T Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE BUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

MAX5101BEUE+T Payment Methods



MAX5101BEUE+T Shipping Methods



If you have any question about MAX5101BEUE+T, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com