



MAX5703AUB+ Information

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For Reference Only

Part Number MAX5703AUB+
Manufacturer Maxim Integrated
Category Integrated Circuits

Integrated Circuits (ICs)
Data Acquisition - Digital to Analog Converters

(DAC)

Description IC DAC 8BIT SPI/SRL 1CH 10UMAX

Package 10-TFSOP, 10-MSOP (0.118", 3.00mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



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MAX5703AUB+ Specifications

Manufacturer Part Number Maxim Integrated Category Integrated Circuits (ICs) Data Acquisition - Digital to Analog Converters (DAC) Package 10-TFSOP, 10-MSOP (0.118", 3.00mm Width) Series Number of Bits Number of D/A Converters 1 Settling Time 2.8µs (Typ) Output Type Voltage - Buffered Differential Output No Data Interface SPI, DSP Reference Type External, Internal Voltage - Supply, Analog 2.7 V ~ 5.5 V Voltage - Supply, Digital 2.7 V ~ 5.5 V INL/DNL (LSB) Architecture		
Category Integrated Circuits (ICs) Data Acquisition - Digital to Analog Converters (DAC) Package 10-TFSOP, 10-MSOP (0.118", 3.00mm Width) Series Number of Bits 8 Number of D/A Converters 1 Settling Time 2.8µs (Typ) Output Type Voltage - Buffered Differential Output No Data Interface SPI, DSP Reference Type External, Internal Voltage - Supply, Analog 2.7 V ~ 5.5 V Voltage - Supply, Digital 12.7 V ~ 5.5 V INL/DNL (LSB) Architecture 1 Integrated Circuits (ICs) Data Acquisition - Digital to Analog Converters (DAC) 8 External, Internal 2.8µs (Typ) Voltage - Supply, Analog 2.7 V ~ 5.5 V Voltage - Supply, Digital 2.7 V ~ 5.5 V Voltage - Supply, Digital 2.7 V ~ 5.5 V	Manufacturer Part Number	MAX5703AUB+
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Package 10-TFSOP, 10-MSOP (0.118", 3.00mm Width) Series - Number of Bits 8 Number of D/A Converters 1 Settling Time 2.8 μ s (Typ) Output Type Voltage - Buffered Differential Output No Data Interface SPI, DSP Reference Type External, Internal Voltage - Supply, Analog 2.7 V ~ 5.5 V Voltage - Supply, Digital 2.7 V ~ 5.5 V INL/DNL (LSB) \pm 0.05, \pm 0.05 Architecture -	Category	Integrated Circuits (ICs)
Series - Number of Bits 8 Number of D/A Converters 1 Settling Time 2.8μs (Typ) Output Type Voltage - Buffered Differential Output No Data Interface SPI, DSP Reference Type External, Internal Voltage - Supply, Analog 2.7 V ~ 5.5 V Voltage - Supply, Digital 2.7 V ~ 5.5 V INL/DNL (LSB) ±0.05, ±0.05 Architecture -		Data Acquisition - Digital to Analog Converters (DAC)
Number of Bits 8 Number of D/A Converters 1 Settling Time 2.8 μ s (Typ) Output Type Voltage - Buffered Differential Output No Data Interface SPI, DSP Reference Type External, Internal Voltage - Supply, Analog 2.7 V ~ 5.5 V Voltage - Supply, Digital 2.7 V ~ 5.5 V INL/DNL (LSB) $\pm 0.05, \pm 0.05$ Architecture -	Package	10-TFSOP, 10-MSOP (0.118", 3.00mm Width)
Number of D/A Converters1Settling Time $2.8\mu s$ (Typ)Output TypeVoltage - BufferedDifferential OutputNoData InterfaceSPI, DSPReference TypeExternal, InternalVoltage - Supply, Analog $2.7 \text{ V} \sim 5.5 \text{ V}$ Voltage - Supply, Digital $2.7 \text{ V} \sim 5.5 \text{ V}$ INL/DNL (LSB) $\pm 0.05, \pm 0.05$ Architecture-	Series	-
Settling Time 2.8 μ s (Typ) Output Type Voltage - Buffered Differential Output No Data Interface SPI, DSP Reference Type External, Internal Voltage - Supply, Analog 2.7 V ~ 5.5 V Voltage - Supply, Digital 2.7 V ~ 5.5 V INL/DNL (LSB) $\pm 0.05, \pm 0.05$ Architecture -	Number of Bits	8
Output TypeVoltage - BufferedDifferential OutputNoData InterfaceSPI, DSPReference TypeExternal, InternalVoltage - Supply, Analog $2.7 \text{ V} \sim 5.5 \text{ V}$ Voltage - Supply, Digital $2.7 \text{ V} \sim 5.5 \text{ V}$ INL/DNL (LSB) $\pm 0.05, \pm 0.05$ Architecture-	Number of D/A Converters	1
Differential OutputNoData InterfaceSPI, DSPReference TypeExternal, InternalVoltage - Supply, Analog $2.7 \text{ V} \sim 5.5 \text{ V}$ Voltage - Supply, Digital $2.7 \text{ V} \sim 5.5 \text{ V}$ INL/DNL (LSB) $\pm 0.05, \pm 0.05$ Architecture-	Settling Time	2.8µs (Typ)
Data InterfaceSPI, DSPReference TypeExternal, InternalVoltage - Supply, Analog $2.7 \text{ V} \sim 5.5 \text{ V}$ Voltage - Supply, Digital $2.7 \text{ V} \sim 5.5 \text{ V}$ INL/DNL (LSB) $\pm 0.05, \pm 0.05$ Architecture-	Output Type	Voltage - Buffered
Reference TypeExternal, InternalVoltage - Supply, Analog $2.7 \text{ V} \sim 5.5 \text{ V}$ Voltage - Supply, Digital $2.7 \text{ V} \sim 5.5 \text{ V}$ INL/DNL (LSB) $\pm 0.05, \pm 0.05$ Architecture-	Differential Output	No
Voltage - Supply, Analog $2.7 \text{ V} \sim 5.5 \text{ V}$ Voltage - Supply, Digital $2.7 \text{ V} \sim 5.5 \text{ V}$ INL/DNL (LSB) $\pm 0.05, \pm 0.05$ Architecture -	Data Interface	SPI, DSP
Voltage - Supply, Digital $2.7 \text{ V} \sim 5.5 \text{ V}$ INL/DNL (LSB) $\pm 0.05, \pm 0.05$ Architecture -	Reference Type	External, Internal
$ \begin{array}{ll} \text{INL/DNL (LSB)} & \pm 0.05, \pm 0.05 \\ \text{Architecture} & - \end{array} $	Voltage - Supply, Analog	2.7 V ~ 5.5 V
Architecture -	Voltage - Supply, Digital	2.7 V ~ 5.5 V
	INL/DNL (LSB)	$\pm 0.05, \pm 0.05$
Operating Temperature 40°C 125°C	Architecture	-
Operating reinperature -40 C ~ 123 C	Operating Temperature	-40°C ~ 125°C
Package / Case 10-TFSOP, 10-MSOP (0.118", 3.00mm Width)	Package / Case	10-TFSOP, 10-MSOP (0.118", 3.00mm Width)
Supplier Device Package 10-uMAX	Supplier Device Package	10-uMAX
Mounting Type -	Mounting Type	-
Report error		Report errors?

MAX5703AUB+ Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

MAX5703AUB+ Payment Methods



















MAX5703AUB+ Shipping Methods













If you have any question about MAX5703AUB+, please do not hesitate to contact us!

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