

MAX6072BAUB41+T

MAX6072BAUB41+T Information



For Reference Only

Part Number MAX6072BAUB41+T

Manufacturer Maxim Integrated

Category Integrated Circuits (ICs)
PMIC - Voltage Reference

Description IC VREF SERIES 4.096V/2.05V

Package 10-TFSOP, 10-MSOP (0.118", 3.00mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









MAX6072BAUB41+T Specifications

Manufacturer Part Number	MAX6072BAUB41+T	
Manufacturer	Maxim Integrated	
Category	Integrated Circuits (ICs)	
	PMIC - Voltage Reference	
Package	10-TFSOP, 10-MSOP (0.118", 3.00mm Width)	
Series	-	
Reference Type	Series	
Output Type	Fixed	
Voltage - Output (Min/Fixed)	2.048V, 4.096V	
Voltage - Output (Max)	-	
Current - Output	10mA	
Tolerance	±0.08%	
Temperature Coefficient	8ppm/°C	
Noise - 0.1Hz to 10Hz	6.4μVp-p, 9.6μVp-p	
Noise - 10Hz to 10kHz	8.6μVrms, 12μVrms	
Voltage - Input	2.7 V ~ 5.5 V	
Current - Supply	280μΑ, 350μΑ	
Current - Cathode	-	
Operating Temperature	-40°C ~ 125°C (TA)	
Mounting Type	Surface Mount	
Package / Case	10-TFSOP, 10-MSOP (0.118", 3.00mm Width)	
Supplier Device Package	10-uMAX	
		Report errors?

MAX6072BAUB41+T Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

MAX6072BAUB41+T Payment Methods



















MAX6072BAUB41+T Shipping Methods













If you have any question about MAX6072BAUB41+T, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com