

MAX8743EEI+

Quote

MAX8743EEI+ Information

	Part Number	MAX8743EEI+	
www.senterner.com	Manufacturer	Maxim Integrated	
	Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Controllers	
	Description	IC REG CTRLR BUCK 28QSOP	Mana
	Package	28-SSOP (0.154", 3.90mm Width)	E B
For Reference Only		For the pricing/inventory/lead time, please contact	
		us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



MAX8743EEI+ Specifications

Manufacturer Part Number	MAX8743EEI+		
Manufacturer	Maxim Integrated		
Category	Integrated Circuits (ICs)		
	PMIC - Voltage Regulators - DC DC Switching Controllers		
Package	28-SSOP (0.154", 3.90mm Width)		
Series	-		
Output Type	Transistor Driver		
Function	Step-Down		
Output Configuration	Positive		
Topology	Buck		
Number of Outputs	2		
Output Phases	1		
Voltage - Supply (Vcc/Vdd)	2 V ~ 28 V		
Frequency - Switching	200kHz, 300kHz, 420kHz, 540kHz		
Duty Cycle (Max)	-		
Synchronous Rectifier	Yes		
Clock Sync	No		
Serial Interfaces	-		
Control Features	Current Limit, Enable, On Time Control, Power Good, Pulse Skipping		
Operating Temperature	-40°C ~ 85°C (TA)		
Package / Case	28-SSOP (0.154", 3.90mm Width)		
Supplier Device Package	28-QSOP		
		Report errors?	

MAX8743EEI+ Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

MAX8743EEI+ Payment Methods





If you have any question about MAX8743EEI+, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com