

**MC100EL52DT Information**


For Reference Only

**Part Number** [MC100EL52DT](#)  
**Manufacturer** ON Semiconductor  
**Category** Integrated Circuits (ICs)  
[Logic - Flip Flops](#)  
**Description** IC D-TYPE POS TRG SNGL 8TSSOP  
**Package** 8-TSSOP, 8-MSOP (0.118", 3.00mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)



[Request a Quote](#)

**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**MC100EL52DT Specifications**

Manufacturer Part Number	<a href="#">MC100EL52DT</a>
Manufacturer	ON Semiconductor
Category	Integrated Circuits (ICs) <a href="#">Logic - Flip Flops</a>
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Series	100EL
Function	Standard
Type	D-Type
Output Type	Differential
Number of Elements	1
Number of Bits per Element	1
Clock Frequency	2.8GHz
Max Propagation Delay @ V, Max CL	-
Trigger Type	Positive, Negative
Current - Output High, Low	-
Voltage - Supply	-4.2 V ~ -5.7 V
Current - Quiescent (Iq)	25mA
Input Capacitance	-
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)

[Report errors?](#)

## MC100EL52DT Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## MC100EL52DT Payment Methods



## MC100EL52DT Shipping Methods



If you have any question about MC100EL52DT, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)