

**MC100LVEL05DTG Information**


For Reference Only

**Part Number** [MC100LVEL05DTG](#)  
**Manufacturer** ON Semiconductor  
**Category** Integrated Circuits (ICs)  
[Logic - Gates and Inverters - Multi-Function, Configurable](#)  
**Description** IC GATE AND/NAND ECL 2INP 8TSSOP  
**Package** 8-TSSOP, 8-MSOP (0.118", 3.00mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**MC100LVEL05DTG Specifications**

Manufacturer Part Number	<a href="#">MC100LVEL05DTG</a>
Manufacturer	ON Semiconductor
Category	Integrated Circuits (ICs) <a href="#">Logic - Gates and Inverters - Multi-Function, Configurable</a>
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Series	100LVEL
Logic Type	AND/NAND Gate
Number of Circuits	1
Number of Inputs	2 Input (1, 1)
Schmitt Trigger Input	No
Output Type	Differential
Current - Output High, Low	-
Voltage - Supply	3 V ~ 3.8 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Supplier Device Package	8-TSSOP

[Report errors?](#)

## MC100LVEL05DTG Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## MC100LVEL05DTG Payment Methods



## MC100LVEL05DTG Shipping Methods



If you have any question about MC100LVEL05DTG, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)