

**MC10EPT20DTR2G Information**


For Reference Only

**Part Number** [MC10EPT20DTR2G](#)  
**Manufacturer** ON Semiconductor  
**Category** Integrated Circuits (ICs)  
[Logic - Translators, Level Shifters](#)  
**Description** IC XLATOR TTL/CMOS-PECL 8-TSSOP  
**Package** 8-TSSOP, 8-MSOP (0.118", 3.00mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)



[Request a Quote](#)

**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**MC10EPT20DTR2G Specifications**

Manufacturer Part Number	<a href="#">MC10EPT20DTR2G</a>
Manufacturer	ON Semiconductor
Category	Integrated Circuits (ICs) <a href="#">Logic - Translators, Level Shifters</a>
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Series	10EPT
Translator Type	Mixed Signal
Channel Type	Unidirectional
Number of Circuits	1
Channels per Circuit	1
Voltage - VCCA	-
Voltage - VCCB	-
Input Signal	LVC MOS, LV TTL
Output Signal	LV PECL
Output Type	Differential
Data Rate	-
Operating Temperature	-40°C ~ 85°C (TA)
Features	-
Mounting Type	Surface Mount
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Supplier Device Package	8-TSSOP

[Report errors?](#)

## MC10EPT20DTR2G Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## MC10EPT20DTR2G Payment Methods



## MC10EPT20DTR2G Shipping Methods



If you have any question about MC10EPT20DTR2G, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)