

## MC10H113MEL Information



For Reference Only

**Part Number** [MC10H113MEL](#)  
**Manufacturer** ON Semiconductor  
**Category** Integrated Circuits (ICs)  
[Logic - Gates and Inverters](#)  
**Description** IC GATE XOR 4CH 2-INP 16-SOEIAJ  
**Package** 16-SOIC (0.209", 5.30mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)



[Request a Quote](#)

## Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



## MC10H113MEL Specifications

Manufacturer Part Number	<a href="#">MC10H113MEL</a>
Manufacturer	ON Semiconductor
Category	Integrated Circuits (ICs) <a href="#">Logic - Gates and Inverters</a>
Package	16-SOIC (0.209", 5.30mm Width)
Series	10H
Logic Type	XOR (Exclusive OR)
Number of Circuits	4
Number of Inputs	2
Features	-
Voltage - Supply	-4.94 V ~ -5.46 V
Current - Quiescent (Max)	-
Current - Output High, Low	50mA, 50mA
Logic Level - Low	-1.48V
Logic Level - High	-1.13V
Max Propagation Delay @ V, Max CL	1.8ns @ -5.2V, -
Operating Temperature	0°C ~ 75°C
Mounting Type	Surface Mount
Supplier Device Package	16-SOEIAJ
Package / Case	16-SOIC (0.209", 5.30mm Width)
<a href="#">Report errors?</a>	

## MC10H113MEL Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## MC10H113MEL Payment Methods



## MC10H113MEL Shipping Methods



If you have any question about MC10H113MEL, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)