

**MC74LVX74M Information**


For Reference Only

**Part Number** [MC74LVX74M](#)  
**Manufacturer** ON Semiconductor  
**Category** Integrated Circuits (ICs)  
[Logic - Flip Flops](#)  
**Description** IC D-TYPE POS TRG DUAL 14SOEIAJ  
**Package** 14-SOIC (0.209", 5.30mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**MC74LVX74M Specifications**

Manufacturer Part Number	<a href="#">MC74LVX74M</a>
Manufacturer	ON Semiconductor
Category	Integrated Circuits (ICs) <a href="#">Logic - Flip Flops</a>
Package	14-SOIC (0.209", 5.30mm Width)
Series	74LVX
Function	Set(Preset) and Reset
Type	D-Type
Output Type	Differential
Number of Elements	2
Number of Bits per Element	1
Clock Frequency	85MHz
Max Propagation Delay @ V, Max CL	13.2ns @ 3.3V, 50pF
Trigger Type	Positive Edge
Current - Output High, Low	4mA, 4mA
Voltage - Supply	2 V ~ 3.6 V
Current - Quiescent (Iq)	2µA
Input Capacitance	4pF
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	14-SOIC (0.209", 5.30mm Width)

[Report errors?](#)

## MC74LVX74M Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## MC74LVX74M Payment Methods



## MC74LVX74M Shipping Methods



If you have any question about MC74LVX74M, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)