

Heisener.com

MCIMX537CVV8C2R2

MCIMX537CVV8C2R2 Information

Part Number MCIMX537CVV8C2R2

Manufacturer NXP

Category Integrated Circuits (ICs)

Embedded - Microprocessors

Description I.MX53 32-BIT MPU ARM CORTEX-A8

Package 529-FBGA

For the pricing/inventory/lead time, please contact

us

For Reference Only Website: https://www.heisener.com

E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









MCIMX537CVV8C2R2 Specifications

Manufacturer Part Number	MCIMX537CVV8C2R2
Manufacturer	NXP
Category	Integrated Circuits (ICs)
	Embedded - Microprocessors
Package	529-FBGA
Series	i.MX53
Core Processor	ARM® Cortex®-A8
Number of Cores/Bus Width	1 Core, 32-Bit
Speed	800MHz
Co-Processors/DSP	Multimedia; NEON™ SIMD
RAM Controllers	LPDDR2, DDR2, DDR3
Graphics Acceleration	Yes
Display & Interface Controllers	Keypad, LCD
Ethernet	10/100 Mbps (1)
SATA	SATA 1.5Gbps (1)
USB	USB 2.0 (2), USB 2.0 + PHY (2)
Voltage - I/O	1.3V, 1.8V, 2.775V, 3.3V
Operating Temperature	-40°C ~ 85°C (TA)
Security Features	ARM TZ, Boot Security, Cryptography, RTIC, Secure Fusebox, Secure JTAG, Secure Memory, Secure RTC, Tamper Detection
Package / Case	529-FBGA
Supplier Device Package	529-FBGA (19x19)

MCIMX537CVV8C2R2 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

MCIMX537CVV8C2R2 Payment Methods



















MCIMX537CVV8C2R2 Shipping Methods













If you have any question about MCIMX537CVV8C2R2, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com