

MCP1652RT-E/MS Information


For Reference Only

Part Number MCP1652RT-E/MS
Manufacturer Microchip Technology
Category Integrated Circuits (ICs)
 PMIC - Voltage Regulators - DC DC Switching Controllers
Description IC REG CTRLR MULT TOPOLOGY 8MSOP
Package 8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


MCP1652RT-E/MS Specifications

Manufacturer Part Number	MCP1652RT-E/MS
Manufacturer	Microchip Technology
Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Controllers
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Series	-
Output Type	Transistor Driver
Function	Step-Up, Step-Up/Step-Down
Output Configuration	Positive
Topology	Boost, Flyback, SEPIC
Number of Outputs	1
Output Phases	1
Voltage - Supply (Vcc/Vdd)	2.7 V ~ 5.5 V
Frequency - Switching	750kHz
Duty Cycle (Max)	80%
Synchronous Rectifier	No
Clock Sync	No
Serial Interfaces	-
Control Features	Current Limit, Enable, Low Battery, Power Good
Operating Temperature	-40°C ~ 125°C (TJ)
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Supplier Device Package	8-MSOP

[Report errors?](#)

MCP1652RT-E/MS Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

MCP1652RT-E/MS Payment Methods



MCP1652RT-E/MS Shipping Methods



If you have any question about MCP1652RT-E/MS, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: salesdept@heisener.com