

MIC5219-2.5BMM-TR

MIC5219-2.5BMM-TR Information

Winn betterner von	 MIC5219-2.5BMM-TR Microchip Technology Integrated Circuits (ICs) PMIC - Voltage Regulators - Linear IC REG LINEAR 2.5V 500MA 8MSOP 8-TSSOP, 8-MSOP (0.118", 3.00mm Width) For the pricing/inventory/lead time, please contact us	
For Reference Only	Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



MIC5219-2.5BMM-TR Specifications

Manufacturer Part Number	MIC5219-2.5BMM-TR	
Manufacturer	Microchip Technology	
Category	Integrated Circuits (ICs)	
	PMIC - Voltage Regulators - Linear	
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)	
Series	-	
Output Configuration	Positive	
Output Type	Fixed	
Number of Regulators	1	
Voltage - Input (Max)	12V	
Voltage - Output (Min/Fixed)	2.5V	
Voltage - Output (Max)	-	
Voltage Dropout (Max)	0.6V @ 500mA	
Current - Output	500mA	
Current - Quiescent (Iq)	-	
Current - Supply (Max)	8μΑ	
PSRR	75dB (120Hz)	
Control Features	Enable	
Protection Features	Over Current, Over Temperature, Reverse Polarity	
Operating Temperature	-40°C ~ 125°C	
Mounting Type	Surface Mount	
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)	
Supplier Device Package	8-MSOP	
		Report errors?

MIC5219-2.5BMM-TR Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

DISCOVER

MIC5219-2.5BMM-TR Payment Methods



MIC5219-2.5BMM-TR Shipping Methods



If you have any question about MIC5219-2.5BMM-TR, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com