

MIC94355-SYMT-TR

MIC94355-SYMT-TR Information

Part Number	MIC94355-SYMT-TR	
Manufacturer	Microchip Technology	
Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - Linear	
Description	IC REG LINEAR 3.3V 500MA 6TDFN	
Package	6-UFDFN Exposed Pad	
	For the pricing/inventory/lead time, please contact	
	us	
	Website: https://www.heisener.com	
	E-mail: salesdept@heisener.com	



Request a Quote

For Reference Only

Heisener.com

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



MIC94355-SYMT-TR Specifications

Manufacturer Part Number	MIC94355-SYMT-TR
Manufacturer	Microchip Technology
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - Linear
Package	6-UFDFN Exposed Pad
Series	Ripple Blocker?
Output Configuration	Positive
Output Type	Fixed
Number of Regulators	1
Voltage - Input (Max)	3.6V
Voltage - Output (Min/Fixed)	3.3V
Voltage - Output (Max)	-
Voltage Dropout (Max)	0.2V @ 500mA
Current - Output	500mA
Current - Quiescent (Iq)	-
Current - Supply (Max)	250μΑ
PSRR	85dB ~ 50dB (100Hz ~ 5MHz)
Control Features	Enable
Protection Features	Over Current, Over Temperature
Operating Temperature	-40°C ~ 125°C
Mounting Type	Surface Mount
Package / Case	6-UFDFN Exposed Pad
Supplier Device Package	6-TDFN (1.6x1.6)
	Report errors?

MIC94355-SYMT-TR Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

DISCOVER

MIC94355-SYMT-TR Payment Methods



MIC94355-SYMT-TR Shipping Methods



If you have any question about MIC94355-SYMT-TR, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com