

MK2703BSLFTR

MK2703BSLFTR Information

	Part Number	MK2703BSLFTR	
www.thrisener.com	Manufacturer	IDT, Integrated Device Technology Inc	E1753:E1
	Category	Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
	Description	IC CLOCK SYNTHESIZER PLL 8-SOIC	- 1 9-19-19
	Package	8-SOIC (0.154", 3.90mm Width)	i ∎ini sheki
For Reference Only		For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



MK2703BSLFTR Specifications

Manufacturer Part Number	MK2703BSLFTR		
Manufacturer	IDT, Integrated Device Technology Inc		
Category	Integrated Circuits (ICs)		
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers		
Package	8-SOIC (0.154", 3.90mm Width)		
Series	-		
Туре	PLL Clock Synthesizer		
PLL	Yes with Bypass		
Input	Clock, Crystal		
Output	CMOS		
Number of Circuits	1		
Ratio - Input:Output	2:2		
Differential - Input:Output	No/No		
Frequency - Max	27MHz		
Divider/Multiplier	No/No		
Voltage - Supply	3.13 V ~ 5.5 V		
Operating Temperature	0° C ~ 70° C		
Mounting Type	Surface Mount		
Package / Case	8-SOIC (0.154", 3.90mm Width)		
Supplier Device Package	8-SOIC		
	Report errors		

MK2703BSLFTR Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE BUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

MK2703BSLFTR Payment Methods



MK2703BSLFTR Shipping Methods



If you have any question about MK2703BSLFTR, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com