

**MK2712STR Information**


For Reference Only

**Part Number** [MK2712STR](#)  
**Manufacturer** IDT, Integrated Device Technology Inc  
**Category** Integrated Circuits (ICs)  
 Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers  
**Description** IC CLK SOURCE MTSC/PAL 8-SOIC  
**Package** 8-SOIC (0.154", 3.90mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**MK2712STR Specifications**

Manufacturer Part Number	<a href="#">MK2712STR</a>
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers
Package	8-SOIC (0.154", 3.90mm Width)
Series	-
Type	Clock Synthesizer - Video
PLL	Yes
Input	Clock
Output	CMOS
Number of Circuits	1
Ratio - Input:Output	1:1
Differential - Input:Output	No/No
Frequency - Max	35.469MHz
Divider/Multiplier	No/No
Voltage - Supply	4.5 V ~ 5.5 V
Operating Temperature	0°C ~ 70°C
Mounting Type	Surface Mount
Package / Case	8-SOIC (0.154", 3.90mm Width)
Supplier Device Package	8-SOIC

[Report errors?](#)

## MK2712STR Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## MK2712STR Payment Methods



## MK2712STR Shipping Methods



If you have any question about MK2712STR, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)