



### **MK3711DMILF Information**

Part Number MK3711DMILF

Manufacturer IDT, Integrated Device Technology Inc

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description** IC VCXO DTV/SET-TOP 8-SOIC **Package** 8-SOIC (0.154", 3.90mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

# **Certified Quality**

For Reference Only

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









# **MK3711DMILF Specifications**

Manufacturer Part Number	MK3711DMILF	
Manufacturer	IDT, Integrated Device Technology Inc	
Category	Integrated Circuits (ICs)	
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
Package	8-SOIC (0.154", 3.90mm Width)	
Series	-	
Type	Voltage Controlled Crystal Oscillator (VCXO)	
PLL	No	
Input	Crystal	
Output	CMOS	
Number of Circuits	1	
Ratio - Input:Output	2:1	
Differential - Input:Output	No/No	
Frequency - Max	16MHz	
Divider/Multiplier	No/No	
Voltage - Supply	3.15 V ~ 3.45 V	
Operating Temperature	-40°C ~ 85°C	
Mounting Type	Surface Mount	
Package / Case	8-SOIC (0.154", 3.90mm Width)	
Supplier Device Package	8-SOIC	
	Re	eport errors?

#### **MK3711DMILF Guarantees**



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

### **MK3711DMILF Payment Methods**



















### **MK3711DMILF Shipping Methods**













If you have any question about MK3711DMILF, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com