

**MK74CG117BFLF Information**


For Reference Only

**Part Number** [MK74CG117BFLF](#)  
**Manufacturer** IDT, Integrated Device Technology Inc  
**Category** Integrated Circuits (ICs)  
[Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers](#)  
**Description** IC CLK DVR PLL 20MHZ 48SSOP  
**Package** 48-BSSOP (0.295", 7.50mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**MK74CG117BFLF Specifications**

Manufacturer Part Number	<a href="#">MK74CG117BFLF</a>
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers</a>
Package	48-BSSOP (0.295", 7.50mm Width)
Series	-
Type	Clock Generator
PLL	Yes with Bypass
Input	Clock, Crystal
Output	Clock
Number of Circuits	1
Ratio - Input:Output	1:17
Differential - Input:Output	No/No
Frequency - Max	100MHz
Divider/Multiplier	Yes/Yes
Voltage - Supply	3.14 V ~ 3.63 V
Operating Temperature	0°C ~ 70°C
Mounting Type	Surface Mount
Package / Case	48-BSSOP (0.295", 7.50mm Width)
Supplier Device Package	48-SSOP

[Report errors?](#)

## MK74CG117BFLF Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## MK74CG117BFLF Payment Methods



## MK74CG117BFLF Shipping Methods



If you have any question about MK74CG117BFLF, please do not hesitate to contact us!

Website: <https://www.heisener.com>

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