



### MKL05Z32VFK4R Information

Heisener.com

Part Number MKL05Z32VFK4R

Manufacturer NXP

Category Integrated Circuits (ICs)

Embedded - Microcontrollers

**Description** KINETIS KL05: 48MHZ CORTEX-M0+ U

Package 24-VFQFN Exposed Pad

For the pricing/inventory/lead time, please contact

us

For Reference Only Website: https://www.heisener.com

E-mail: salesdept@heisener.com



Request a Quote

## **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## MKL05Z32VFK4R Specifications

Manufacturer Part Number	MKL05Z32VFK4R	
Manufacturer	NXP	
Category	Integrated Circuits (ICs)	
	Embedded - Microcontrollers	
Package	24-VFQFN Exposed Pad	
Series	Kinetis KL0	
Core Processor	ARM® Cortex®-M0+	
Core Size	32-Bit	
Speed	48MHz	
Connectivity	I <sup>2</sup> C, SPI, UART/USART	
Peripherals	Brown-out Detect/Reset, DMA, LVD, POR, PWM, WDT	
Number of I/O	22	
Program Memory Size	32KB (32K x 8)	
Program Memory Type	FLASH	
EEPROM Size	-	
RAM Size	4K x 8	
Voltage - Supply (Vcc/Vdd)	1.71 V ~ 3.6 V	
Data Converters	A/D 12x12b, D/A 1x12b	
Oscillator Type	Internal	
Operating Temperature	-40°C ~ 105°C (TA)	
Mounting Type	-	
Package / Case	24-VFQFN Exposed Pad	
Supplier Device Package	24-QFN (4x4)	
		Report errors?

#### MKL05Z32VFK4R Guarantees



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

### MKL05Z32VFK4R Payment Methods



















# MKL05Z32VFK4R Shipping Methods













If you have any question about MKL05Z32VFK4R, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com