

MP2362DF-LF-Z

MP2362DF-LF-Z Information

warth the con	Part Number Manufacturer Category	MP2362DF-LF-Z Monolithic Power Systems Inc. Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Regulators	
	Description	IC REG BUCK ADJ 2A DL	
	Package	20-TSSOP (0.173", 4.40mm Width) Exposed Pad	- 同時登録者
For Reference Only		For the pricing/inventory/lead time, please contact	
		Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



MP2362DF-LF-Z Specifications

Manufacturer Part Number	MP2362DF-LF-Z
Manufacturer	Monolithic Power Systems Inc.
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - DC DC Switching Regulators
Package	20-TSSOP (0.173", 4.40mm Width) Exposed Pad
Series	-
Function	Step-Down
Output Configuration	Positive
Topology	Buck
Output Type	Adjustable
Number of Outputs	2
Voltage - Input (Min)	4.75V
Voltage - Input (Max)	23V
Voltage - Output (Min/Fixed)	1.22V
Voltage - Output (Max)	16V
Current - Output	2A
Frequency - Switching	380kHz
Synchronous Rectifier	No
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	20-TSSOP (0.173", 4.40mm Width) Exposed Pad
Supplier Device Package	20-TSSOPF
	Report errors'

MP2362DF-LF-Z Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE BUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

MP2362DF-LF-Z Payment Methods



MP2362DF-LF-Z Shipping Methods



If you have any question about MP2362DF-LF-Z, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com