

**MPC9330ACR2 Information**


For Reference Only

**Part Number** [MPC9330ACR2](#)  
**Manufacturer** IDT, Integrated Device Technology Inc  
**Category** Integrated Circuits (ICs)  
[Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers](#)  
**Description** IC PLL CLOCK GENERATOR 32-LQFP  
**Package** 32-LQFP  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**MPC9330ACR2 Specifications**

Manufacturer Part Number	<a href="#">MPC9330ACR2</a>
Manufacturer	IDT, Integrated Device Technology Inc
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers</a>
Package	32-LQFP
Series	-
Type	PLL Clock Generator
PLL	Yes with Bypass
Input	LVC MOS, Crystal
Output	LVC MOS
Number of Circuits	1
Ratio - Input:Output	3:6
Differential - Input:Output	No/No
Frequency - Max	120MHz
Divider/Multiplier	Yes/No
Voltage - Supply	3.135 V ~ 3.465 V
Operating Temperature	0°C ~ 70°C
Mounting Type	Surface Mount
Package / Case	32-LQFP
Supplier Device Package	32-LQFP (7x7)

[Report errors?](#)

## MPC9330ACR2 Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## MPC9330ACR2 Payment Methods



## MPC9330ACR2 Shipping Methods



If you have any question about MPC9330ACR2, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)