

MPC962305EJ-1HR2 Information

-	Part Number	MPC962305EJ-1HR2	
- 177	Manufacturer	IDT, Integrated Device Technology Inc	
www.hersener.com	Category	Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
	Description	IC BUFFER ZD 1:5 3.3V 8-TSSOP	639178%m
	Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)	
For Reference Only		For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



MPC962305EJ-1HR2 Specifications

	Report errors	\$?	
Supplier Device Package	8-TSSOP		
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)		
Mounting Type	Surface Mount		
Operating Temperature	-40°C ~ 85°C		
Voltage - Supply	3 V ~ 3.6 V		
Divider/Multiplier	No/No		
Frequency - Max	133.33MHz		
Differential - Input:Output	No/No		
Ratio - Input:Output	1:5		
Number of Circuits	1		
Output	LVCMOS		
Input	LVCMOS		
PLL	Yes		
Туре	Fanout Buffer (Distribution), Zero Delay Buffer		
Series	-		
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)		
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers		
Category	Integrated Circuits (ICs)		
Manufacturer	IDT, Integrated Device Technology Inc		
Manufacturer Part Number	MPC962305EJ-1HR2		

MPC962305EJ-1HR2 Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE BUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

MPC962305EJ-1HR2 Payment Methods





If you have any question about MPC962305EJ-1HR2, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com