

**NB3N2302DR2G Information**


For Reference Only

**Part Number** [NB3N2302DR2G](#)  
**Manufacturer** ON Semiconductor  
**Category** Integrated Circuits (ICs)  
[Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers](#)  
**Description** IC FREQ MULTIPLIER ZDB 8SOIC  
**Package** 8-SOIC (0.154", 3.90mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**NB3N2302DR2G Specifications**

Manufacturer Part Number	<a href="#">NB3N2302DR2G</a>
Manufacturer	ON Semiconductor
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers</a>
Package	8-SOIC (0.154", 3.90mm Width)
Series	-
Type	-
PLL	Yes
Input	LVC MOS, LV TTL
Output	LVC MOS, LV TTL
Number of Circuits	1
Ratio - Input:Output	1:2
Differential - Input:Output	No/No
Frequency - Max	133MHz
Divider/Multiplier	No/Yes
Voltage - Supply	3.135 V ~ 5.5 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	8-SOIC (0.154", 3.90mm Width)
Supplier Device Package	8-SOIC

[Report errors?](#)

## NB3N2302DR2G Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## NB3N2302DR2G Payment Methods



## NB3N2302DR2G Shipping Methods



If you have any question about NB3N2302DR2G, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)