

**NB3N508SDTG Information**


For Reference Only

**Part Number** [NB3N508SDTG](#)  
**Manufacturer** ON Semiconductor  
**Category** Integrated Circuits (ICs)  
 Clock/Timing - Clock Generators, PLLs,  
 Frequency Synthesizers  
**Description** IC CLK GEN VCXO M-LVDS 16-TSSOP  
**Package** 16-TSSOP (0.173", 4.40mm Width)  
 For the pricing/inventory/lead time, please contact  
 us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**NB3N508SDTG Specifications**

Manufacturer Part Number	<a href="#">NB3N508SDTG</a>
Manufacturer	ON Semiconductor
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers</a>
Package	16-TSSOP (0.173", 4.40mm Width)
Series	PureEdge?
Type	Clock Generator
PLL	Yes
Input	Crystal
Output	M-LVDS
Number of Circuits	1
Ratio - Input:Output	1:1
Differential - Input:Output	No/Yes
Frequency - Max	216MHz
Divider/Multiplier	Yes/No
Voltage - Supply	3.135 V ~ 3.465 V
Operating Temperature	0°C ~ 70°C
Mounting Type	Surface Mount
Package / Case	16-TSSOP (0.173", 4.40mm Width)
Supplier Device Package	16-TSSOP

[Report errors?](#)

## NB3N508SDTG Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## NB3N508SDTG Payment Methods



## NB3N508SDTG Shipping Methods



If you have any question about NB3N508SDTG, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)