

NC7SP74K8X

a Quote

NC7SP74K8X Information

www.netsener.com	Part Number	NC7SP74K8X	
	Manufacturer	ON Semiconductor	តា ១៨
	Category	Integrated Circuits (ICs) Logic - Flip Flops	
	Description	IC FF D-TYPE SNGL 1BIT US8	
	Package	8-VFSOP (0.091", 2.30mm Width)	日常にあ
		For the pricing/inventory/lead time, please contact us	1217771
For Reference Only		Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request
For Reference Only	-	IC FF D-TYPE SNGL 1BIT US8 8-VFSOP (0.091", 2.30mm Width) For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com	i

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



NC7SP74K8X Specifications

Manufacturer Part Number	NC7SP74K8X
Manufacturer	ON Semiconductor
Category	Integrated Circuits (ICs)
	Logic - Flip Flops
Package	8-VFSOP (0.091", 2.30mm Width)
Series	7SP
Function	Set(Preset) and Reset
Туре	D-Type
Output Type	Differential
Number of Elements	1
Number of Bits per Element	1
Clock Frequency	150MHz
Max Propagation Delay @ V, Max CL	8ns @ 3.3V, 30pF
Trigger Type	Positive Edge
Current - Output High, Low	2.6mA, 2.6mA
Voltage - Supply	0.9 V ~ 3.6 V
Current - Quiescent (Iq)	900nA
Input Capacitance	2pF
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount
Package / Case	8-VFSOP (0.091", 2.30mm Width)
	Report errors?

NC7SP74K8X Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

NC7SP74K8X Payment Methods





If you have any question about NC7SP74K8X, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com