

NTLUD3A50PZTBG

NTLUD3A50PZTBG Information



For Reference Only

Part Number NTLUD3A50PZTBG Manufacturer ON Semiconductor

Category Discrete Semiconductor Products
Transistors - FETs, MOSFETs - Arrays

Description MOSFET 2P-CH 20V 2.8A UDFN

Package 6-UDFN Exposed Pad

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









NTLUD3A50PZTBG Specifications

	Report errors?
Supplier Device Package	6-UDFN (2x2)
Package / Case	6-UDFN Exposed Pad
Mounting Type	Surface Mount
Operating Temperature	-55°C ~ 150°C (TJ)
Power - Max	500mW
Input Capacitance (Ciss) (Max) @ Vds	920pF @ 15V
Gate Charge (Qg) (Max) @ Vgs	10.4nC @ 4.5V
Vgs(th) (Max) @ Id	1V @ 250μA
Rds On (Max) @ Id, Vgs	50 mOhm @ 4A, 4.5V
Current - Continuous Drain (Id) @ 25°C	2.8A
Drain to Source Voltage (Vdss)	20V
FET Feature	Logic Level Gate
FET Type	2 P-Channel (Dual)
Series	-
Package	6-UDFN Exposed Pad
	Transistors - FETs, MOSFETs - Arrays
Category	Discrete Semiconductor Products
Manufacturer	ON Semiconductor
Manufacturer Part Number	NTLUD3A50PZTBG

NTLUD3A50PZTBG Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

NTLUD3A50PZTBG Payment Methods



















NTLUD3A50PZTBG Shipping Methods













If you have any question about NTLUD3A50PZTBG, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com