

**PI6C2409-1WE Information**


For Reference Only

**Part Number** [PI6C2409-1WE](#)  
**Manufacturer** Diodes Incorporated  
**Category** Integrated Circuits (ICs)  
[Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers](#)  
**Description** IC ZERO DELAY CLOCK BUFF 16SOIC  
**Package** 16-SOIC (0.154", 3.90mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**PI6C2409-1WE Specifications**

Manufacturer Part Number	<a href="#">PI6C2409-1WE</a>
Manufacturer	Diodes Incorporated
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers</a>
Package	16-SOIC (0.154", 3.90mm Width)
Series	-
Type	Zero Delay Buffer
PLL	Yes with Bypass
Input	TTL
Output	TTL
Number of Circuits	1
Ratio - Input:Output	1:9
Differential - Input:Output	No/No
Frequency - Max	133MHz
Divider/Multiplier	No/No
Voltage - Supply	3 V ~ 3.6 V
Operating Temperature	0°C ~ 70°C
Mounting Type	Surface Mount
Package / Case	16-SOIC (0.154", 3.90mm Width)
Supplier Device Package	16-SOIC

[Report errors?](#)

## PI6C2409-1WE Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## PI6C2409-1WE Payment Methods



## PI6C2409-1WE Shipping Methods



If you have any question about PI6C2409-1WE, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)